

# Integrated microwave photonic splitter with reconfigurable amplitude, phase and delay offsets

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**This work presents an integrated microwave photonics splitter with reconfigurable amplitude, phase, and delay offsets. The core components for this function are a dual-parallel Mach-Zehnder modulator, a deinterleaver, and tunable delay lines, all implemented using photonic integrated circuits. Using a demonstrator with an optical free spectral range of 25 GHz, we show experimentally the RF splitting function over two continuous bands, i.e. 0.9–11.6 GHz and 13.4–20 GHz. This result promises a deployable solution for creating wideband, reconfigurable RF splitters in integrated forms.**

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Integrated microwave photonics (IMWP) is providing a new and promising path for RF engineering by utilizing a broad range of photonic functions in integrated forms [1, 2]. Besides the unprecedented features of large instantaneous bandwidth, easy implementation of tuning, and reduced electromagnetic interference, IMWP enables RF functions with small size, weight, as well as power consumption, and allows for software-defined signal processing functions in one integrated device [3] (analogues to programmable electrical signal processors). This shows strong potential of commercial adoption in future RF communication and sensor systems and networks [4, 5].

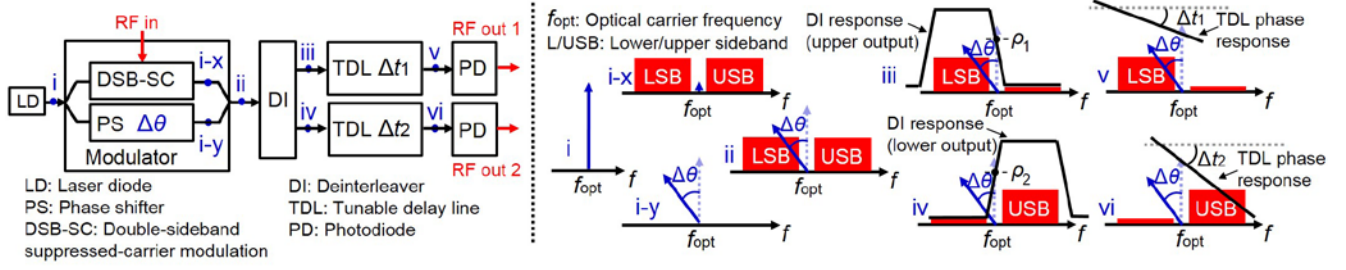
Here, we present a wideband  $1 \times 2$  RF splitter with reconfigurable amplitude, phase, and delay offsets between its two outputs, based on photonic integrated circuits (PICs). While difficult to achieve using conventional electronic integrated circuit technologies due to limited tuning capabilities [6–9], reconfigurable RF splitters can be realized in compact forms, resorting to IMWP technologies. These are a useful function with great application potential. For instance, the splitter can be applied for simultaneous generation of “I” and “Q” signals with a wide frequency coverage for coherent RF communication and radar systems

[10]. Another example is to use the splitter as a reconfigurable feeding network for dual-linear antennas, which provides each antenna component an independent complex coefficient (arbitrary phase and amplitude) to enable electrically-controlled polarization-switching function [11] (e.g. switching between linear and circular polarizations by controlling the inter-component phase difference).

A number of microwave photonic phase shifters and delay lines have been introduced using various devices, such as spatial light modulators [12], fiber and waveguide gratings [13–15], semiconductor optical amplifiers [16, 17], waveguide ring resonators [18, 19], polarization modulators [20, 21], and fibers with stimulated Brillouin scattering [22, 23]. However, when applied into a  $1 \times 2$  arbitrary-offset RF splitter, these approaches will in general lead to doubled system complexity for using two times phase shifters and/or delay lines. Besides, most of them will require independent controls of RF amplitude, phase, and delay for each output, incurring additional engineering effort. Recently, some of us presented the concept of a reconfigurable microwave photonic splitter allowing for arbitrary amplitude and phase offsets [24]. It utilizes a simple tuning mechanism in a dual-parallel Mach-Zehnder modulator (DPMZM) [25], and allows for broadband operation. However, the first demonstration of that concept comprises a bulky and expensive arrangement of discrete photonic devices, such as a spatial light modulator-based optical signal processor and two lasers.

In contrast, the IMWP approach we present in this work reduces the bulky system into a PIC-based solution that requires only one laser and provides delay offset as an additional dimension of reconfigurability. It improves the optical processing resolution to the sub-GHz regime, and has clear potential for single-chip integration as well as for functional extendibility. This owes to the fact that all the constituent components are common photonic integrated circuit building blocks and can be designed with reconfigurability [1, 2].

Figure 1 depicts the system configuration of the RF splitter. A CW laser provides an optical carrier frequency,  $f_{\text{opt}}$ . It is modulated by an RF input such that the output spectrum comprises of two RF sidebands and an optical carrier that has an independently adjustable phase. The process for achieving such modulation spectrum is shown in the insets (i) to (ii) of Fig. 1. For simplicity, we restrict here to the case of small



**Fig. 1.** System configuration and RF-to-RF signal flow of the RF splitter (left), with optical-spectrum illustrations (right).

signal modulation, so that only the first-order sidebands need to be taken into consideration [26]. Here, the optical carrier is first split into two separate paths. In one path, two RF sidebands are created by means of double-sideband suppressed-carrier (DSB-SC) modulation. In the other path, a phase control  $\Delta\theta$  is applied to the optical carrier. Then, the recombination of the two paths results in the desired spectrum. Subsequently, the modulated optical signal is sent to a dedicated deinterleaver, where the whole spectrum is cut into two parts and fed respectively to the two deinterleaver outputs. The filter response of the deinterleaver is designed such that each output let through one of the sidebands and a portion of the optical carrier as shown in the insets (iii) and (iv) of Fig. 1. In effect, each output contains a single-sideband-modulated optical carrier [18]. By means of direct detection, the beat product of the optical carrier and one sideband recovers the input RF signal, with an RF phase shift equal to the phase difference of the higher-frequency beat term relative to the lower-frequency beat term [26]. Then, the beat product of the optical carrier and lower sideband results in an RF phase shift,  $\Delta\theta$ , while the beat product of the upper sideband and optical carrier results in an opposite phase shift,  $-\Delta\theta$ , meaning an RF splitter phase offset of  $2\Delta\theta$ . When  $\rho_1$  and  $\rho_2 = 1 - \rho_1$  are used to indicate the power splitting coefficients of the optical carrier, the ratio,  $\beta = \rho_1/\rho_2$ , governs the RF splitter amplitude offset. In addition, as shown in (v) and (vi) of Fig. 1, a pair of tunable delay lines providing two different delays,  $\Delta t_1$  and  $\Delta t_2$  (corresponding to two different phase slopes), are used to enable delay offset between the two outputs of the RF splitter.

Based on the small-signal assumption, the optical signals at the marked points of the system are given by

$$E_{i-x} = E_0 m(t) (e^{-j2\pi(f_{opt} - f_{RF})t} + e^{-j2\pi(f_{opt} + f_{RF})t}) \quad (1)$$

$$E_{i-y} = E_0 (e^{-j(2\pi f_{opt} t + \Delta\theta)}) \quad (2)$$

$$E_{ii} = E_{i-x} + E_{i-y} = E_0 (e^{-j(2\pi f_{opt} t + \Delta\theta)} + m(t) e^{-j2\pi(f_{opt} - f_{RF})t} + m(t) e^{-j2\pi(f_{opt} + f_{RF})t}) \quad (3)$$

$$E_v = E_0 (\sqrt{\rho_1} e^{-j(2\pi f_{opt} (t + \Delta t_1) + \Delta\theta)} + m(t) e^{-j2\pi(f_{opt} - f_{RF})(t + \Delta t_1)}) \quad (4)$$

$$E_{vi} = E_0 (\sqrt{\rho_2} e^{-j(2\pi f_{opt} (t + \Delta t_2) + \Delta\theta)} + m(t) e^{-j2\pi(f_{opt} + f_{RF})(t + \Delta t_2)}) \quad (5)$$

where  $f_{opt}$  is the frequency of the optical carrier,  $m(t)$  and  $f_{RF}$  are the complex amplitude and center frequency of the input RF signal,  $s(t) = m(t)\cos(2\pi f_{RF}t)$ , and  $E_0$  represents the electric field amplitude of the optical input wave reduced by the losses in the signal paths.  $\rho_n$  ( $n = 1, 2$ ) is the power splitting coefficient of the optical carrier for the two deinterleaver outputs, which is determined by where the optical carrier is positioned in the transition band of the deinterleaver, as shown in Fig. 1.  $\Delta t_n$  ( $n = 1, 2$ ) is the time delay provided by the delay lines. For the sake of simplicity, without loss of generality, we ignore here the initial phases of the optical carrier and RF signals, and assume that the deinterleaver introduces no amplitude and phase distortions

over the sidebands. Then the two photodetector outputs,  $I_{out1}$  and  $I_{out2}$ , can be derived from (4) and (5), respectively, as

$$I_{out1} = \sqrt{\rho_1} \eta m(t) \cos(2\pi f_{RF}(t + \Delta t_1) + \Delta\theta), \quad (6)$$

$$I_{out2} = \sqrt{\rho_2} \eta m(t) \cos(2\pi f_{RF}(t + \Delta t_2) - \Delta\theta), \quad (7)$$

where  $\eta$  represents a common RF amplitude factor determined by the input optical power and photodetector responsivity [26]. Evidently, such a system works as a  $1 \times 2$  RF splitter with the amplitude, phase and delay offsets governed by the parameters,  $\rho_n$ ,  $\Delta\theta$ , and  $\Delta t_n$ . Moreover, in the case that the deinterleaver provides periodic passbands along the frequency, the system explained above is able to operate at multiple RF bands within the bandwidths of the modulator and photodiode.

Figure 2 depicts our implementation of the modulator, deinterleaver and delay lines as photonic integrated circuits. For the DSB-SC modulator and carrier manipulation, we use a dual-parallel Mach-Zehnder modulator (DPMZM) comprising two Mach-Zehnder modulators (MZM1 and MZM2). As illustrated, in the upper arm, MZM1 is driven by the RF input signal and biased (via Bias 1) for DSB-SC modulation; in the lower arm, MZM2 and an independent optical phase shifter are biased (via Bias2 and Bias3, respectively) for controlling the amplitude and phase of the optical carrier.

The deinterleaver is realized via a Mach-Zehnder interferometer assisted with two ring resonators (2RAMZI), with their ring loops being twice as long as the differential arm length of the MZI [11]. The delay line uses a side-coupled integrated spaced sequence of resonators (SCISSOR) [27]. In terms of signal processing, these circuits can be modeled as digital filters [28], whose z-transform transfers are given by

$$\mathbf{H}_{2RAMZI} = \begin{bmatrix} H_{11}(z) & H_{12}(z) \\ H_{21}(z) & H_{22}(z) \end{bmatrix} = \delta \begin{bmatrix} \sqrt{1 - \kappa_R} & -j\sqrt{\kappa_R} \\ -j\sqrt{\kappa_R} & \sqrt{1 - \kappa_R} \end{bmatrix} \begin{bmatrix} A_U(z) & 0 \\ 0 & A_L(z) \end{bmatrix} \begin{bmatrix} \sqrt{1 - \kappa_L} & -j\sqrt{\kappa_L} \\ -j\sqrt{\kappa_L} & \sqrt{1 - \kappa_L} \end{bmatrix} \quad (8)$$

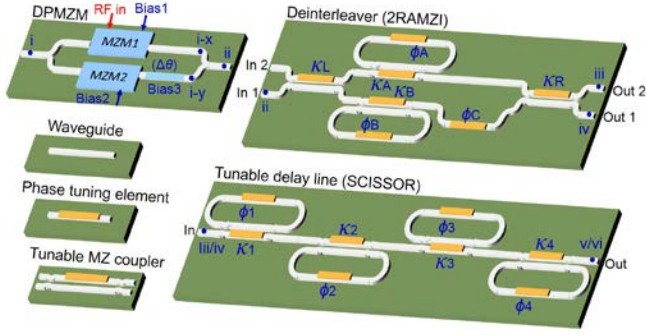
$$A_U(z) = \frac{\sqrt{1 - \kappa_A} - \gamma^2 z^{-2} e^{-j\phi_A}}{1 - \sqrt{1 - \kappa_A} \gamma^2 z^{-2} e^{-j\phi_A}} \quad (9)$$

$$A_L(z) = \frac{\sqrt{1 - \kappa_B} - \gamma^2 z^{-2} e^{-j\phi_B}}{1 - \sqrt{1 - \kappa_B} \gamma^2 z^{-2} e^{-j\phi_B}} \cdot \gamma z^{-1} e^{-j\phi_C} \quad (10)$$

$$H_{SCISSOR}(z) = \chi \sum_{n=1}^N \frac{\sqrt{1 - \kappa_n} - \gamma z^{-2} e^{-j\phi_n}}{1 - \sqrt{1 - \kappa_n} \gamma z^{-2} e^{-j\phi_n}} \quad (11)$$

where  $z = \exp(-j2\pi f/\Delta f_{FSR})$  represents the angular frequency normalized to the free spectral range,  $\Delta f_{FSR}$ , (FSR) of the circuit [28]. In the expressions,  $\gamma$  is the amplitude transmission coefficient determined by the waveguide loss,  $\delta$  and  $\chi$  are complex coefficients which account for the general insertion loss and phase shift introduced by the circuits, parameters  $\kappa$  and  $\phi$  express the power coupling coefficient and phase shift, respectively. For providing circuit reconfigurability [1],  $\kappa$  and  $\phi$  are controlled using phase tuning elements as illustrated in Fig. 2.

To provide an experimental verification, we realized a laboratory demonstrator of the proposed splitter. The setup uses a narrow bandwidth CW laser at 1.5  $\mu\text{m}$  wavelength (AlnairLabs TLG-300M), a



**Fig. 2.** Schematics of PIC implementations of the modulator, deinterleaver and delay line in the system shown in Fig. 1.

DPMZM (Sumitomo 40G DQPSK), and photodiodes (u<sup>2</sup>t XPRV2021A). The deinterleaver and delay lines comprising four cascaded ring resonators are fabricated on a single chip, in a commercial Si<sub>3</sub>N<sub>4</sub> waveguide platform (TriPleX) [29, 30], with a common FSR of 25 GHz. Chromium heaters on top of the waveguide enable thermal-optical phase tuning [1]. The chip coupling is optimized for TE polarization. The total fiber-to-fiber insertion loss amounts to 9 dB, dominated by two times fiber-chip coupling losses of about 4 dB/facet. However, this value is expected to decrease significantly, to about 1dB/facet [31] when a particular design of waveguide facet or interposer is used to optimize the coupling efficiency. Figure 3 depicts the waveguide mask layouts and measured frequency responses of the deinterleaver and delay line. The corresponding circuit parameters are listed in Table 1.

**Table 1.** Circuit parameters for the frequency responses in Fig. 3.

2RAMZI		SCISSOR	
	Value	Value (Delay 0 ns)	Value (Delay 0.2 ns)
$\kappa_L$	0.5	$\kappa_1$	0
$\kappa_R$	0.5	$\phi_1$	Arbitrary
$\kappa_A$	0.87	$\kappa_2$	0
$\phi_A$	$\alpha$	$\phi_2$	Arbitrary
$\kappa_B$	0.32	$\kappa_3$	0
$\phi_B$	$\alpha$	$\phi_3$	Arbitrary
$\phi_C$	$\alpha + \pi/2$	$\kappa_4$	0
		$\phi_4$	Arbitrary
			$\beta + 0.48\pi$

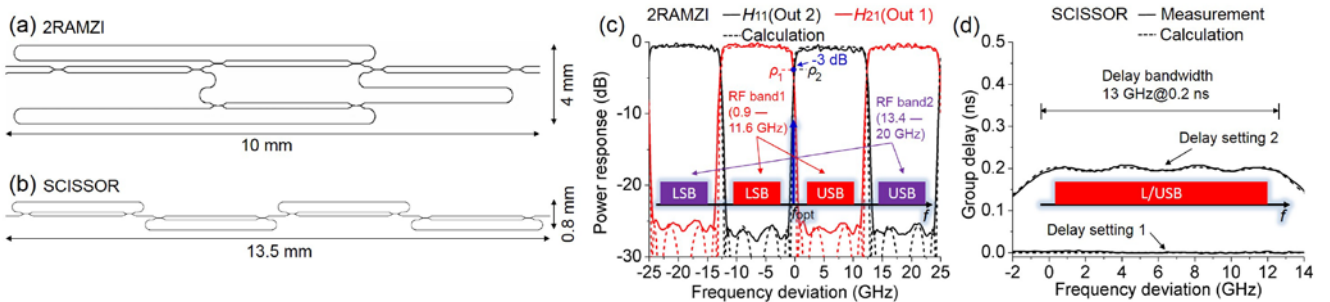
$\alpha$ : phase for aligning the deinterleaver to optical carrier  
 $\beta$ : phase for aligning the delay band to the target RF sideband

As shown in Fig. 3(c), the deinterleaver (2RAMZI) provides periodic passbands, allowing the system to operate at multiple RF bands. However, due to the bandwidth limitation of the modulator and photodiodes in our system, only two of these RF bands are selected in this work. This selection is motivated as follows. As explained in [11], the parameter setting in Table 1 configures the 2RAMZI into a 5<sup>th</sup>-order Chebyshev-Type-II filter. Such a filter is characterized by a flat passband with a -0.5 dB-bandwidth equal to 47% of the FSR, an equal-ripple

stopband with suppression of 25 dB, and a sharp transition bandwidth (from -0.5 dB to -25 dB) equal to 7% of the filter -3dB bandwidth that accounts for half of the FSR. Based on our circuit FSR of 25 GHz, these features translate to a transition bandwidth of 0.9 GHz and an effective RF bandwidth of 10.7 GHz for each deinterleaver passband, considering the optical carrier is positioned within a transition band and both RF sidebands are covered in the -0.5 dB-passbands. Fig. 3(d) depicts the measured two group delay responses of the SCISSOR with delay values of 0 ns and 0.2 ns for a bandwidth of 13 GHz. These are configured to be wider than the deinterleaver passband and therefore guarantee that the entire RF sideband is covered in the delay band. When required, a larger delay tuning range can be provided by increasing the number of ring resonators [1, 27, 28]. Due to the loss in the waveguide, the total loss of the delay line increases in a near-linear manner with the delay value [30]. In our experiment, a delay of 0.2 ns induces a loss about 0.5 dB.

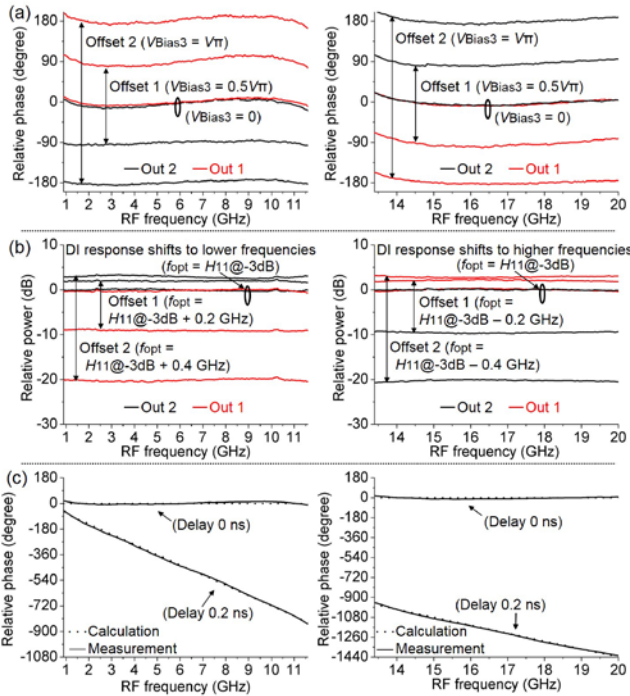
With the DPMZM, deinterleaver, and delay lines properly configured, we performed a measurement of the RF transmission of the demonstrator system (scattering parameter  $S_{21}$ ), using a vector network analyzer (Anritsu 37247D). As shown in Fig. 3c, the optical carrier was initially positioned in the middle between the transition bands of the two deinterleaver outputs, i.e., at the -3-dB position with respect to both  $H_{11}$  and  $H_{21}$ , corresponding to equal optical carrier splitting coefficients,  $\rho_1 = \rho_2$  (Fig. 1). Two RF sweeping bands were used, i.e. 0.9 to 11.6 GHz and 13.4 to 20 GHz (limited by the maximum frequency of the measurement equipment). The measurement system was calibrated to remove the effect of the RF cables. Figure 4(a) depicts the RF phase responses of the system, where the initial phase offset between the two outputs is removed for clarity. For this measurement, the delay lines were bypassed by using the 0-ns setting, and RF phase offset with a continuous and full tuning range of  $2\pi$  was demonstrated by continuously changing the optical carrier phase via Bias3 of the modulator. Figure 4(b) depicts the RF power responses of the system. Here, we demonstrate the function of a tunable amplitude offset, which was performed by varying  $\rho_1$  and  $\rho_2$  through frequency-shifting the deinterleaver response (equivalent to changing the position of the optical carrier in the transition band of the deinterleaver). When the deinterleaver response (Fig. 3c) shifts to a lower frequency,  $\rho_1$  for Out 1 decreases as the optical carrier becomes closer to the stopband of  $H_{21}$ , whereas  $\rho_2$  for Out 2 increases as the optical carrier becomes closer to the passband of  $H_{11}$ . This will be vice versa when the deinterleaver response shifts to a higher frequency. The frequency shift was implemented using the heaters that control  $\phi_{A-C}$ . Figure 4(c) depicts the effect of the delay line. As an example, an output was provided with an additional delay of 0.2 ns as shown in Fig. 3d. Then, an RF phase slope difference is obtained as expected between the two outputs. The delay change was performed using the heaters for  $\kappa_{1-4}$  and  $\phi_{1-4}$ .

The results in Fig. 4 clearly prove the continuous reconfigurability of the amplitude, phase, and delay offsets of the RF splitter function. As all the tuning elements in the system (modulator biases and circuit heaters) are driven by DC voltages, a simple multi-channel DC supply can be used for system configuration. For the voltage control, a micro-controller and a look-up table can be employed as a possible approach



**Fig. 3.** (a, b) waveguide mask layouts of the 2RAMZI and 4-ring SCISSOR; (c) measured power response of the 2RAMZI, with illustration of the frequency alignment with respect to the optical signal spectrum; (d) two measured group delay responses of the SCISSOR.





**Fig. 4.** RF transmission measurements (scattering parameter  $S_{21}$ ) of the system for RF bands 0.9 to 11.6 GHz and 13.4 to 20 GHz: (a) continuous tuning of phase responses ( $V_{\pi}$ : modulator DC halfwave voltage), (b) continuous tuning of power responses, and (c) continuous tuning of phase responses showing the delay effect.

for the parameter tuning process. Our demonstrator chip has a power dissipation about 150 mW/heater (using the heater design as in [1, 18, 19, 30]). However, for such passive-function PICs, alternative implementations of phase tuning have been demonstrated recently with multiple-orders-of-magnitude reductions of power dissipation [32, 33], which are highly promising for practical applications.

The proposed system is in nature a microwave photonic link, therefore achieving a system gain and dynamic range that outperform the current all-electronics RF processing systems remains a challenge [34]. These issues require further improvement of the optoelectronic components [26]. In terms of device realization, the proposed system has the potential to be fully integrated on the chip level. This can be seen from a number of previous works showing the integration of similar PIC building blocks by means of micro-assembly of chips of different technologies [31, 35, 36] or monolithic integration on one waveguide platform such as InP [37].

In conclusion, we have demonstrated a wideband  $1 \times 2$  RF splitter with reconfigurable amplitude, phase and delay offsets. The system is expected to be realized as a stand-alone device and have great application potential in the wideband RF communication systems.

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